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## AXIOM PROJECT PARTENRS



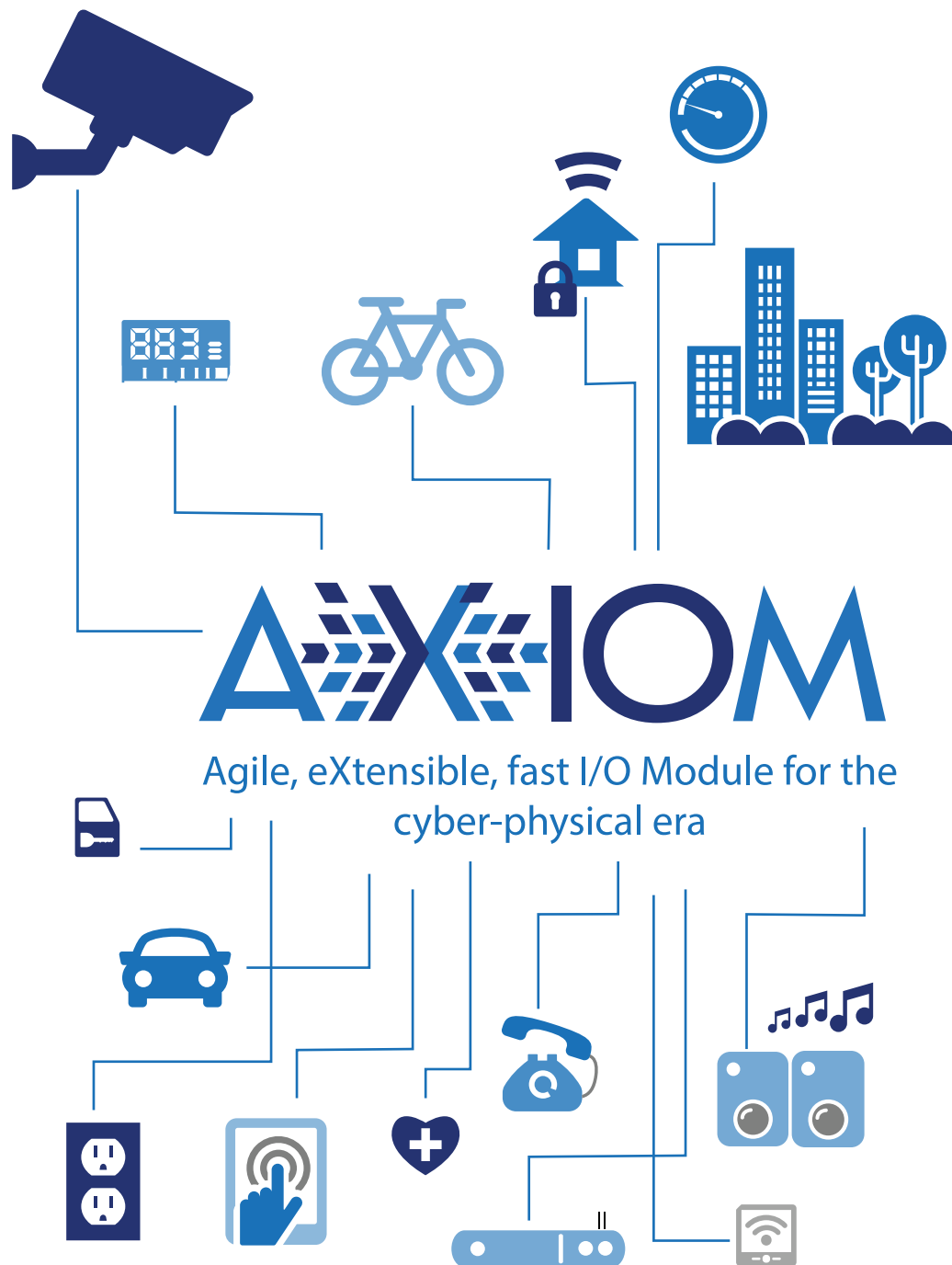
University of Siena  
(Coordinator Partner)



Institute of Computer Science



This project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 645496.



## FLEXIBLE

### ENERGY EFFICIENT AND SCALABLE BOARD

Flexibility: FPGA, fast-and-cheap interconnects based on existing connectors like SATA

Energy efficiency: low-power ARM, FPGA

Multi-node Scalability: board-to-board fast interconnects

## EASY

### PROGRAMMABLE

Programming model: Improved OmpSs

Runtime & OS: improved thread management

Compiler: BSC Mercurium, OS: Linux, Drivers: provided as open-source by partners

## EASY

### INTERFACING WITH THE CYBER-PHYSICAL WORLDS

Platform: integrating also Arduino support for a plenty of pluggable board (so-called "shields")

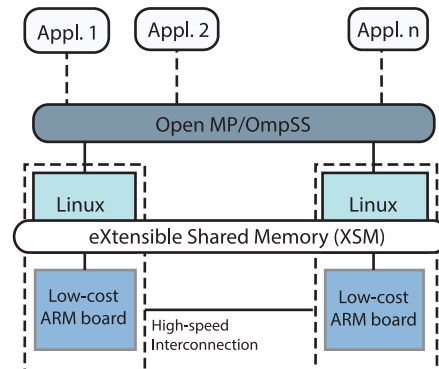
Platform: building on the UDOO experience from SECO



### TECHNOLOGY INNOVATIONS

- Shared
- Distributed
- Modular & scalable
- Multi-platform
- High performance
- Low-cost/Low-power consumption
- Easy programmable & sustainable
- Usability & adaptability constraints
- Open Source

### AXIOM APPROACH



SOFTWARE  
(Algorithms & Programme Languages)

EASY PROGRAMMABILITY

