



Flexible, energy efficient and multi-board

Flexibility: FPGA, fast-and-cheap interconnects based on existing connectors like SATA

Energy efficiency: low-power ARM, FPGA

Modularity: board-to-board fast interconnects



Easily Programmable FPGA

Programming model: Improved OmpSs
Runtime & OS: improved thread management

Compiler: BSC Mercurium, **OS:** Linux,

Drivers: provided as open-source by partners



Easy Interfacing with the Cyber-Physical Worlds

Platform: integrating also Arduino support for a plenty of pluggable board (so-called "shields")

Platform: building on the UDOO experience from SECO

