

Runtime Support for Self-Awareness in Interconnected CPS Systems

Dionisios Pnevmatikatos Dimitris Theodoropoulos {pnevmati, dtheodor}@ics.forth.gr





Computing Systems Week 2015 May 7th, 2015, Oslo, Norway

Title Terms

• Self-awareness:

- Adaptive, self-healing, knowledge of self & environment, goal-oriented
- Deal with uncertainty => change behaviour
- Runtime support: System manager & optimizer
- Interconnected CPSs: System of linked collaborative computational elements



Self-aware Systems

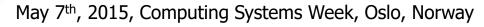
- O Domains:
 - Automotive
 - Aerospace
 - Robotics
 - Healthcare
 - Transportation
 - Energy saving



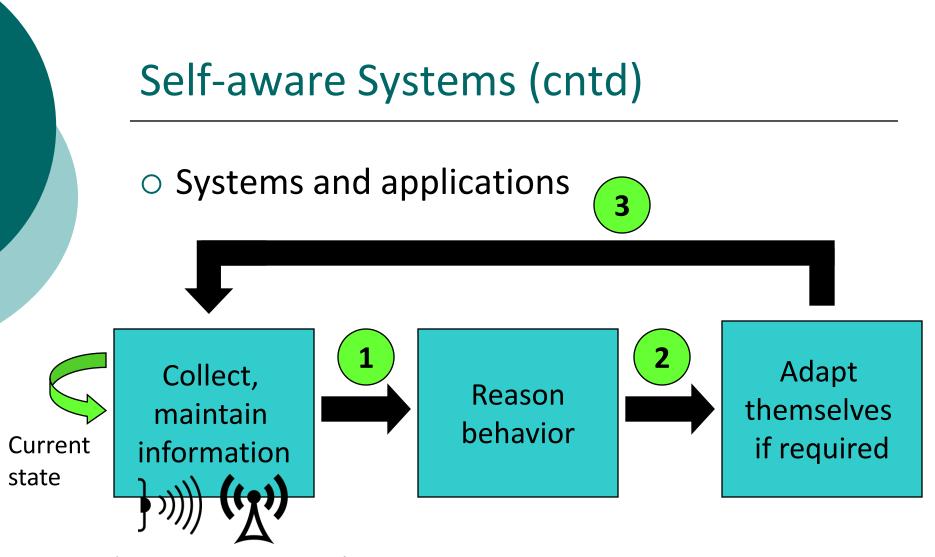












Surrounding environment evaluation



Self-aware Systems (cntd)

System monitoring (self/local/global? state)

Internal system adaptation

- Available resources, current workload, energy budget
- External system adaptation
 - Information exchange, surrounding environmental conditions, application requirements, ...
- (Self-)learning?



Self-aware Systems Projects

- AWARENESS Coordination Action (2010-2013)
- Autonomic Service-Component Ensembles (ASCENS)
- Engineering Proprioception in Computing Systems (EPICS)
- Organic computing
- Relevance and cognition for self-awareness in a contentcentric Internet (RECOGNITION)
- Self-aware Pervasive Service Ecosystems (SAPERE)
- Symbiotic Evolutionary Robot Organisms(SYMBRION)
- Collective Cognitive Robots (CoCoRo)



Required Runtime support

- Towards **RELIABLE** self-aware reconfigurable interconnected CPSs
 - Intelligent task scheduling
 - Software / Hardware tasks
 - Efficient tasks distribution to available nodes
 - Dynamic system reconfiguration (FPGA? Coarser-grain?)
 - Low-overhead runtime kernel
 - Evaluate current state
 - Assess surrounding environment
 - Switch operating modes when necessary

Reliable functionality – Self healing

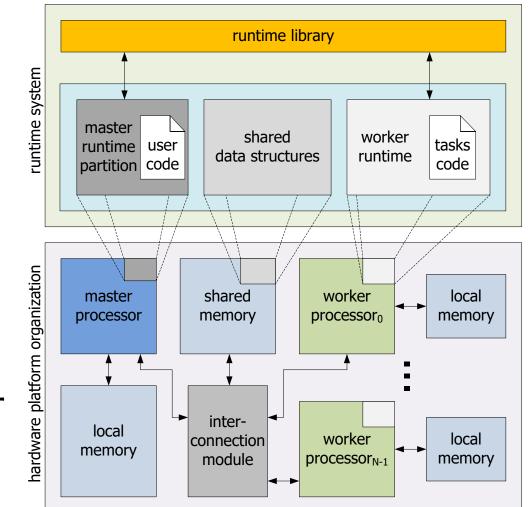


The DeSyRe Runtime System

- Lightweight kernel for *reliable* reconfigurable MPSoCs
- Task-based programming model
 - Address faults at SW/HW, adapt QoS
- System level checkpoint / restore
- Transient errors support
 - Local task variables checkpoint task restart
- Heterogeneous, Adaptive, Supports QoS
 - Flexible expression of optimization goals



- o Master
 - Central, protected controller
- o Workers
 - Do the work
 - Provide redundancy
- Opportunities for adaptation



CPS: the AXIOM project

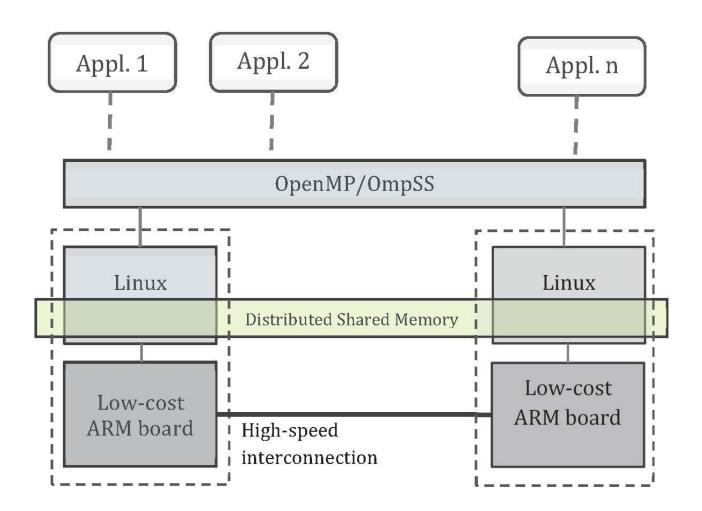
Agile, eXtensible, fast I/O Module, started in Feb. 2015, Contract ID 645496

Aim: develop new HW/SW architectures for CPSs that heavily interact between them and humans

- Real-time system reaction
- Ample computational power
- Energy efficiency
- Modularity / scalability
- Easy programmability



AXIOM Approach





AXIOM Applications

Potential for Self-awareness

- Video-surveillance
- Smart home







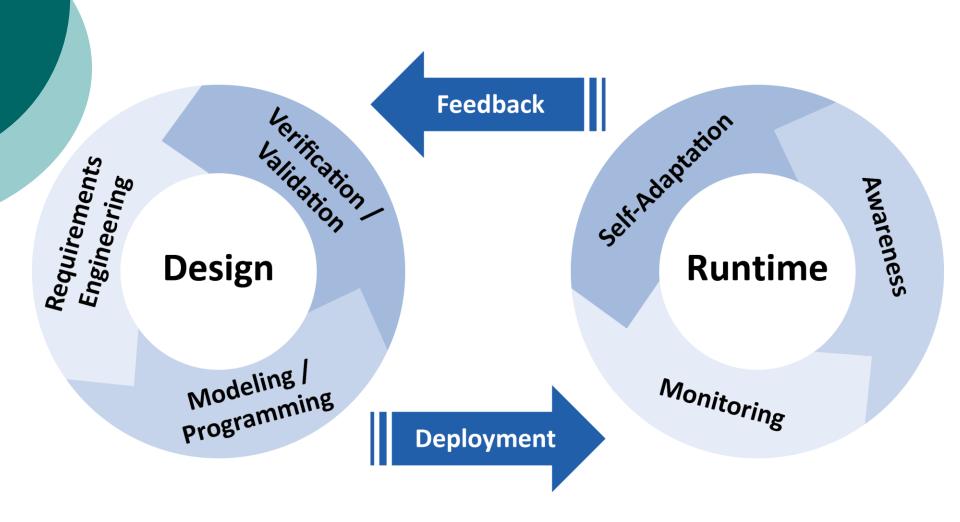
Extending AXIOM with self-awareness

• When adaptivity becomes awareness?

- Al/machine learning/etc.
- limited "palette" of options BUT intractable system level options?
- DeSyRe Runtime is a solid base for adaptation
- Flexible scheduling can accommodate learning
- "Smart" Decision making:
 - Criteria? Application Specific? Machine learning?
- O How to design/debug/verify such a system?



Ascens: Ensemble Development Life Cycle



Where are we?

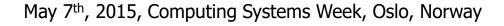
Adaptable systems are hard to design

- O Self-awareness support needed?
 - Provide flexibility/options
 - Provide metrics for estimating cost/benefit
 - Provide mechanisms for using chosen option
 - Debug the nodes and the entire system!
- We are not there yet!



Where are we?

- Q1: Where is this "awareness" in ES expressed?
 SW, HW, or else?
 - at all levels, parts of the global answer, but mostly: runtime (SW)
- O Q2: achieved "self-awareness"?
 - no! But is it really necessary?
- Q3: What does "self-awareness" in ES really mean?
 - dynamic behavior control in large systems





Where are we?

- Q4: current solutions "self-aware" or just smartly designed?
 - majority: smart design
- Q5: "Self" refers to a single entity?
 - self behavior as part of system
- Q6: "Self-awareness is achieved"
 - It is not achieved
 - need tools to consider and support it in the design of complex systems



End of Presentation

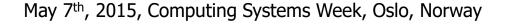
Questions / Discussion





Permanent faults recovery

- Task migration to active workers
- Alternative task implementations
- Generic structure, easily mapped to different shared-memory platforms
 - 8-core Microblaze-based implementation on ML605 FPGA board
 - Recore's platform (Leon + 2 DSP Xentiums + 2 SiMS processors)





Implementation on Recore's platform

